

FIG. 2

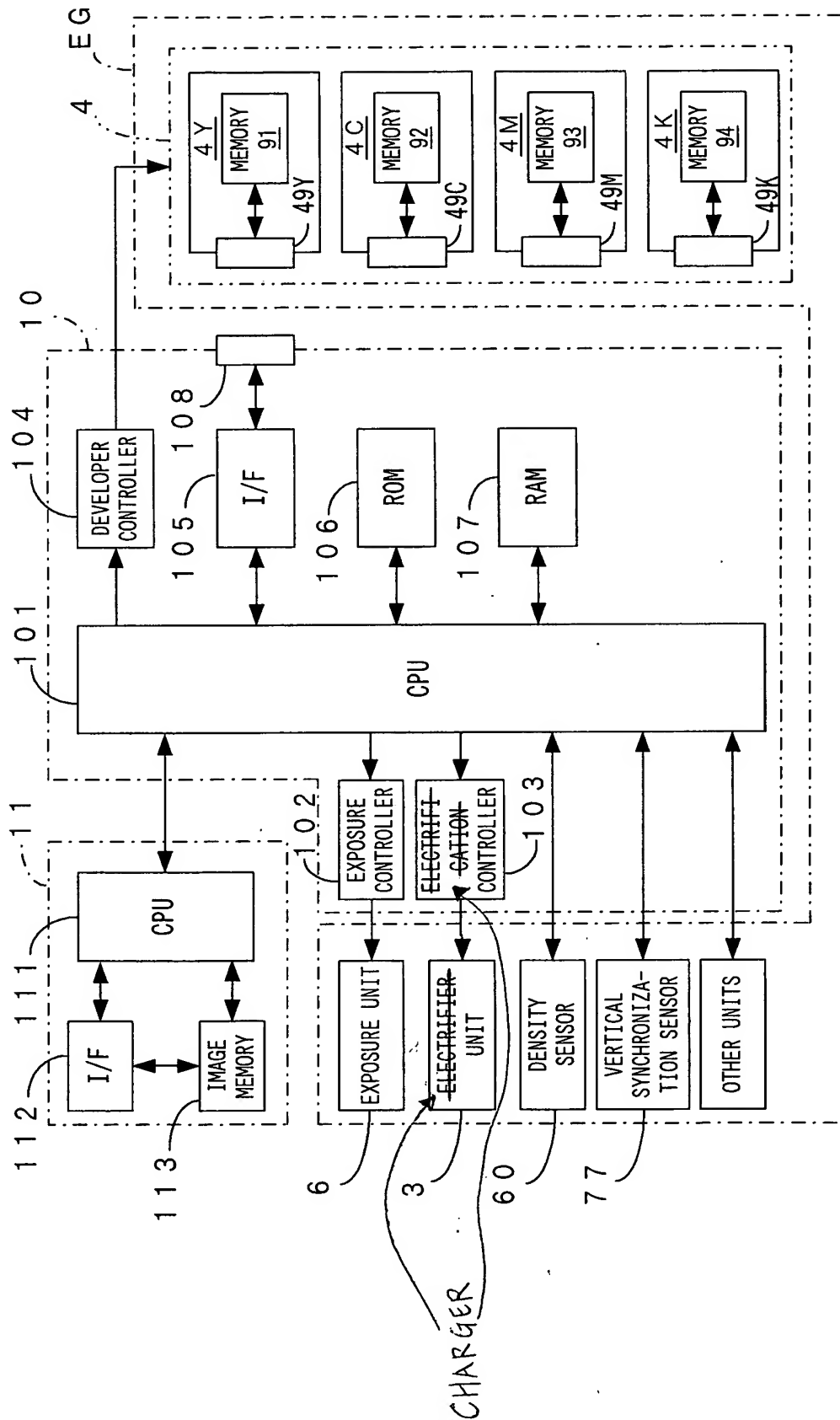


FIG. 15

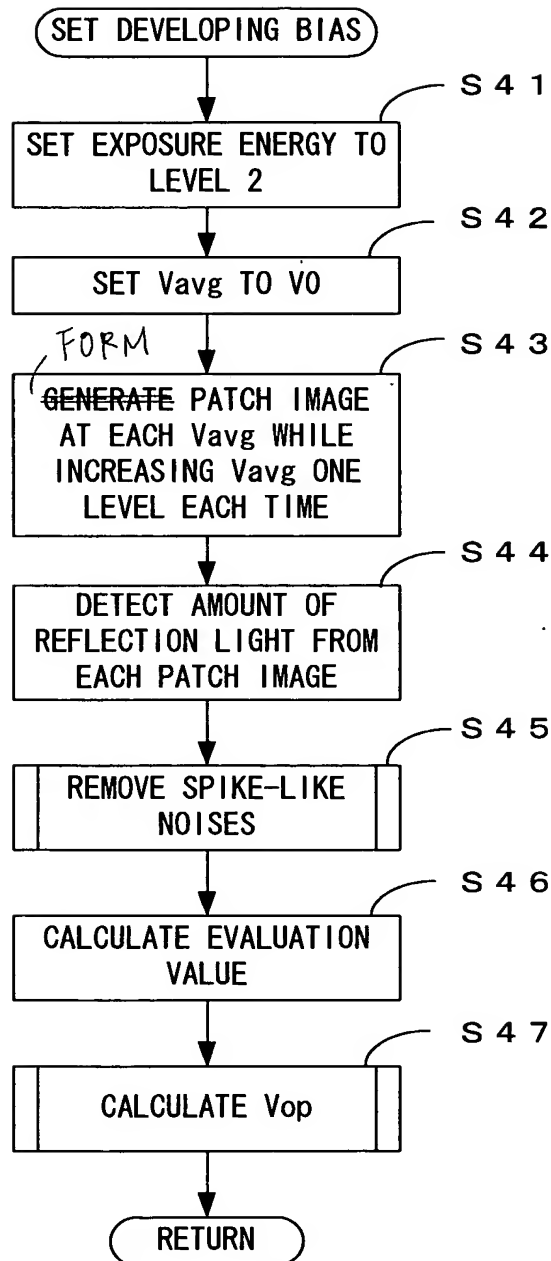


FIG. 16

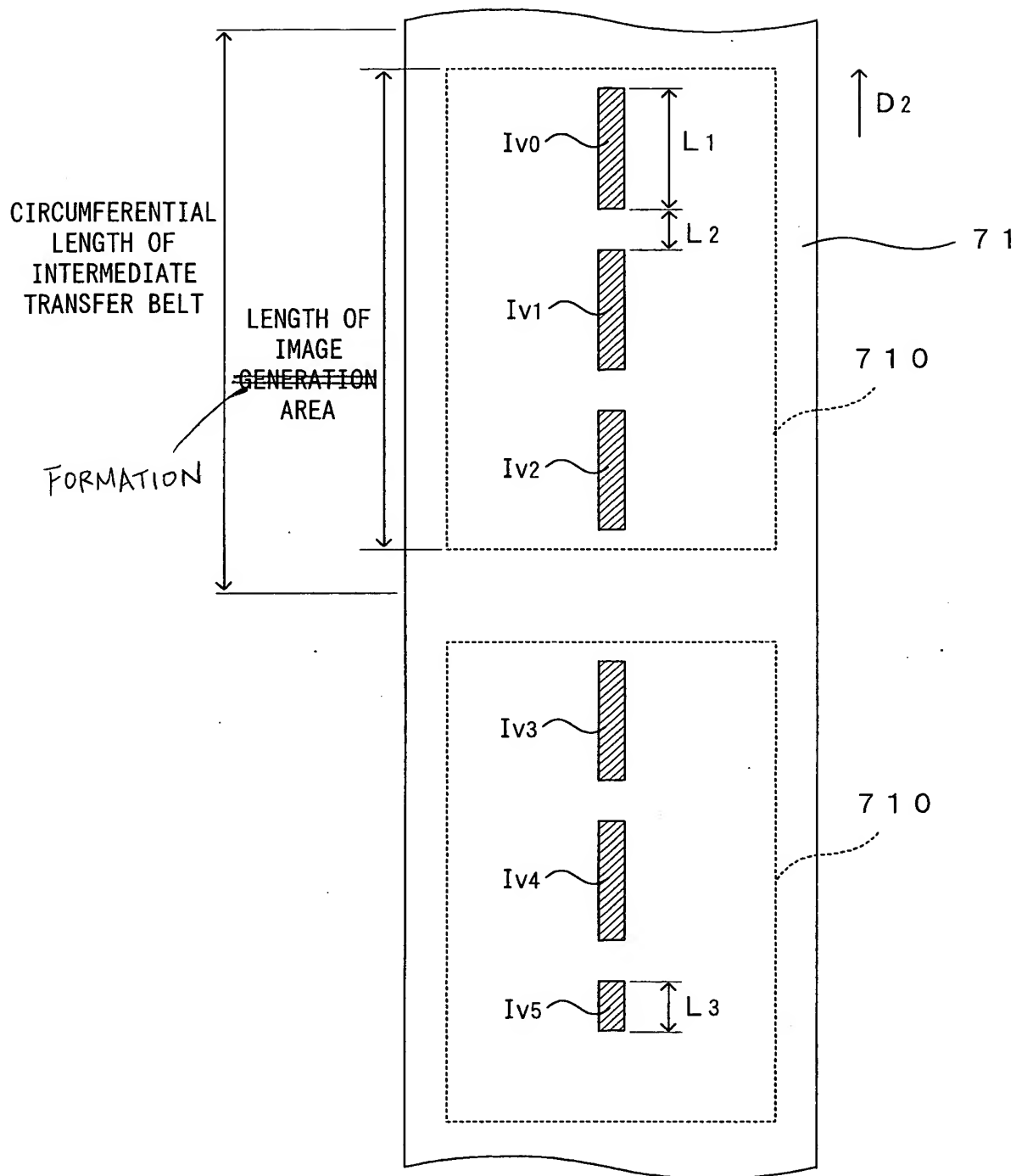


FIG. 19

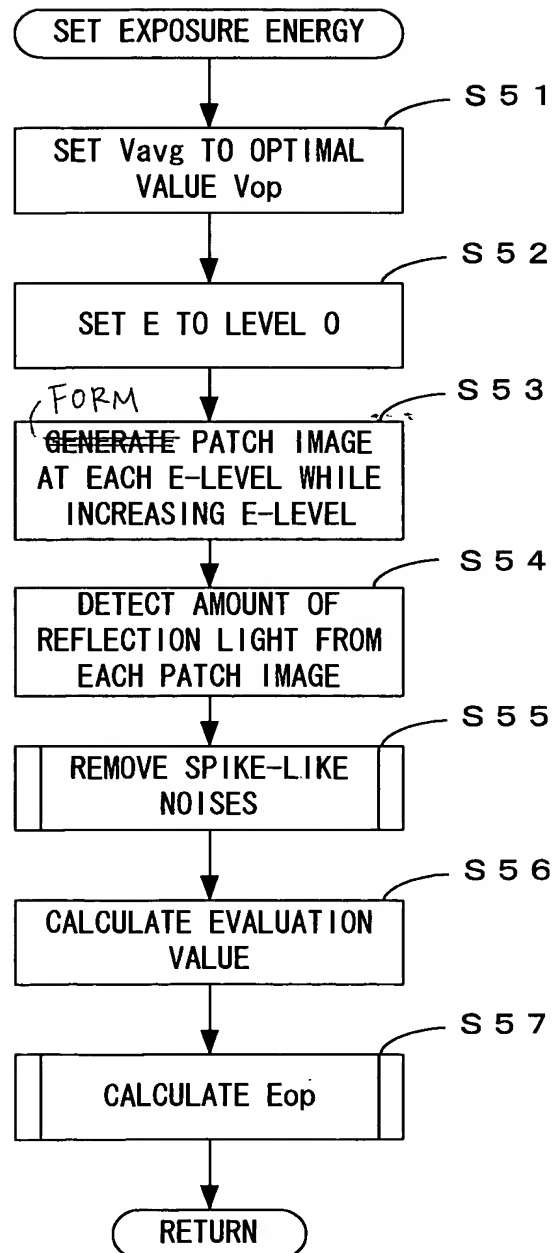


FIG. 20

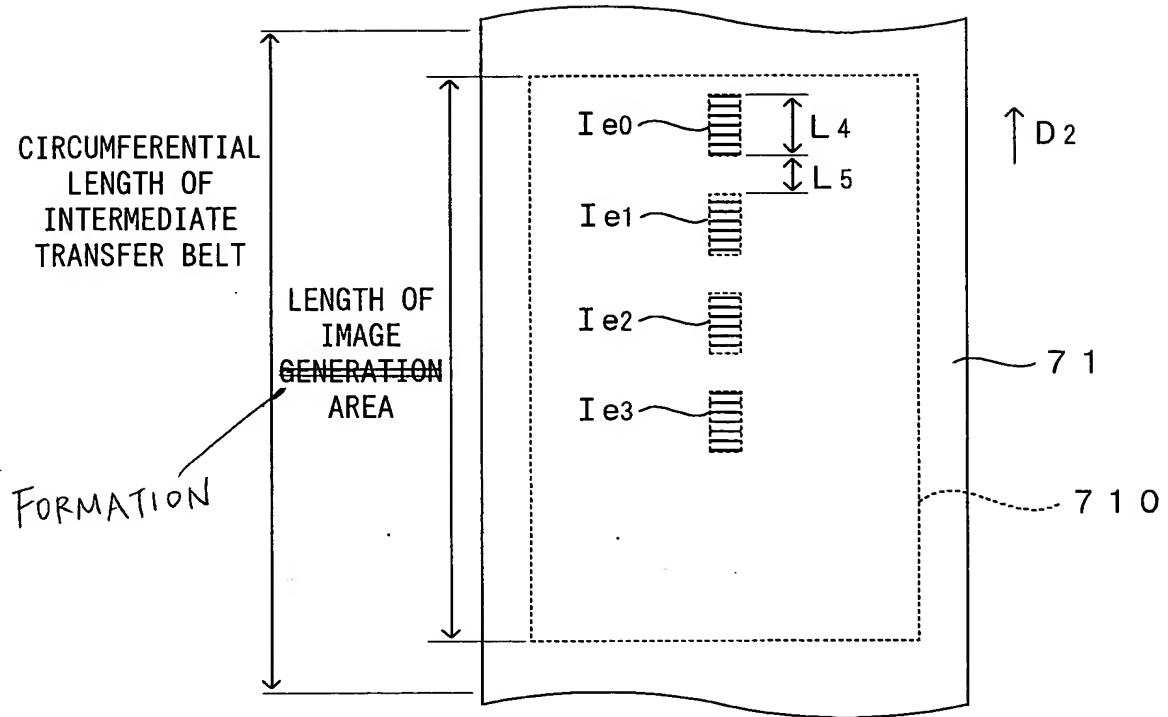


FIG. 23

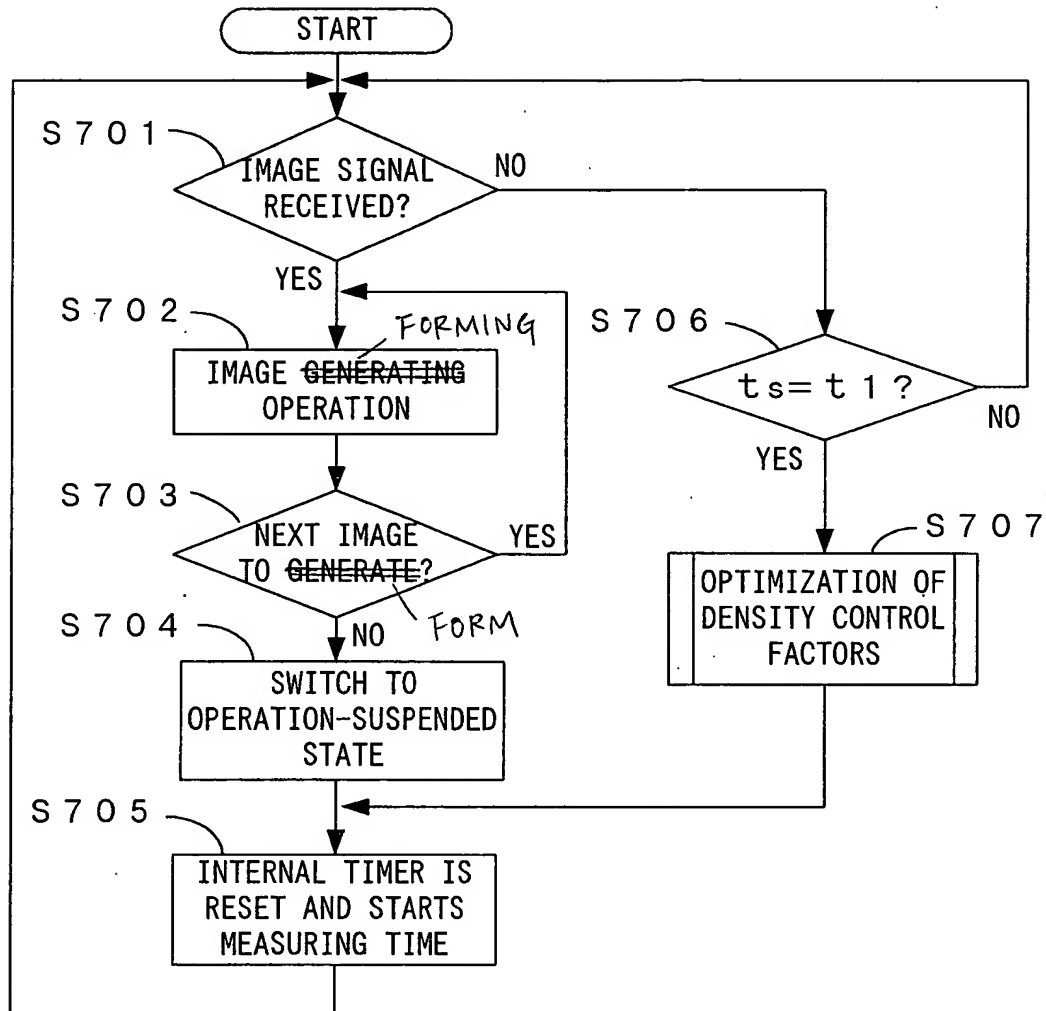


FIG. 24A

$t_s < t_1$

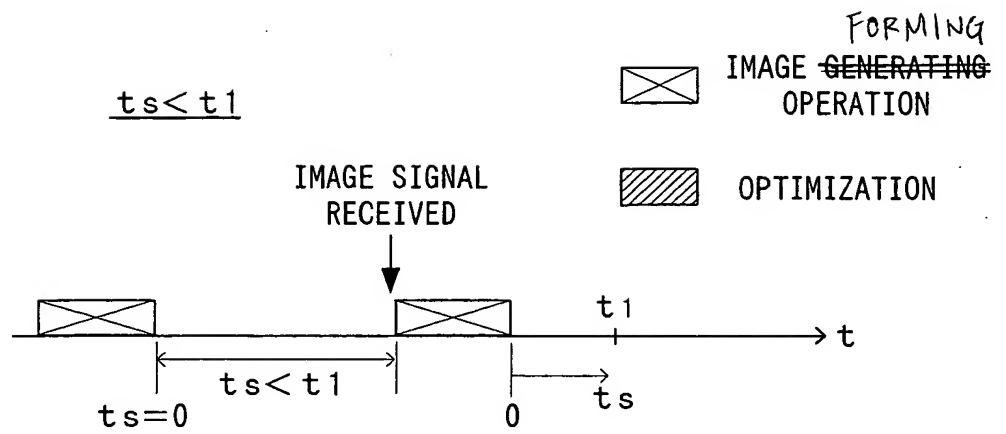


FIG. 24B

$t_s = t_1$

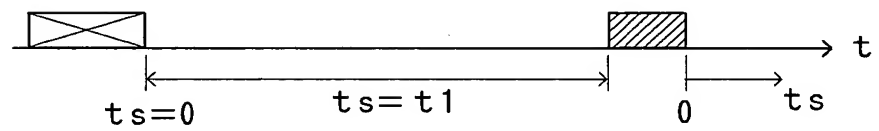


FIG. 25

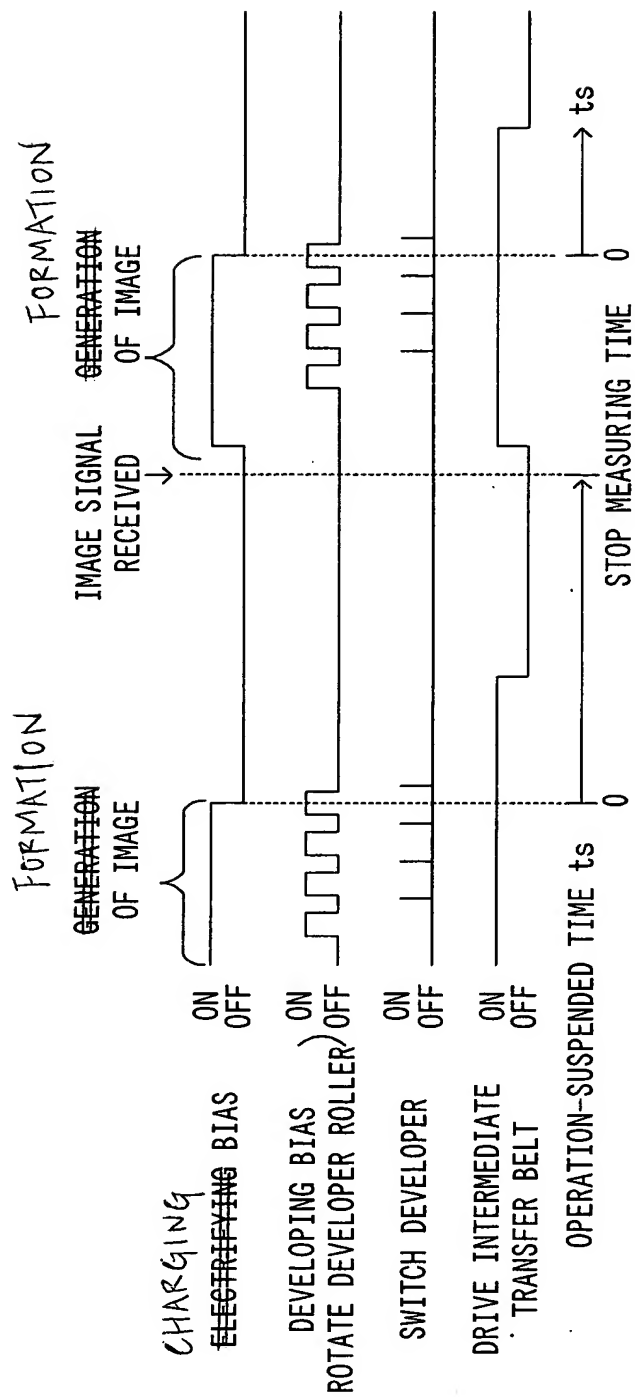
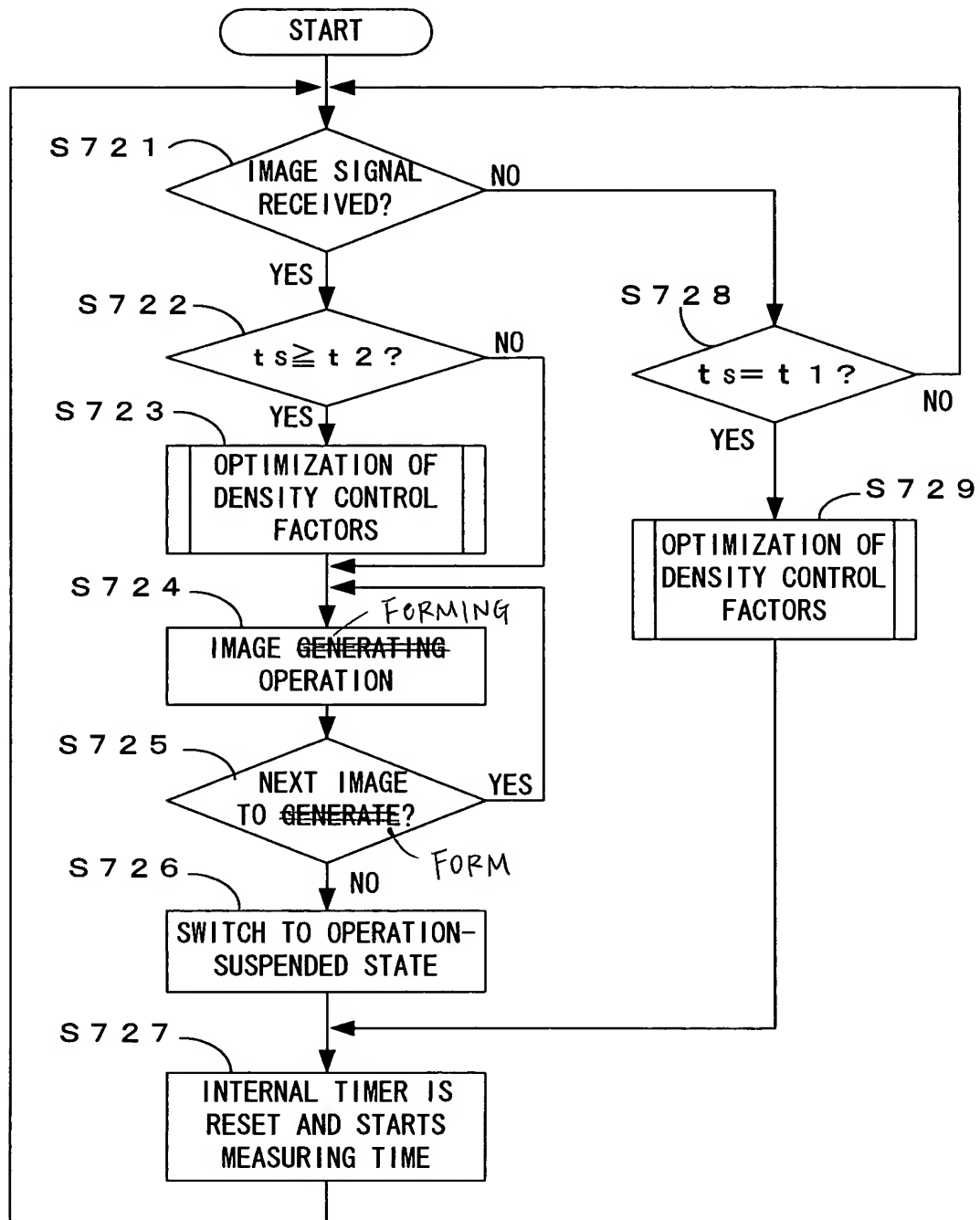


FIG. 26



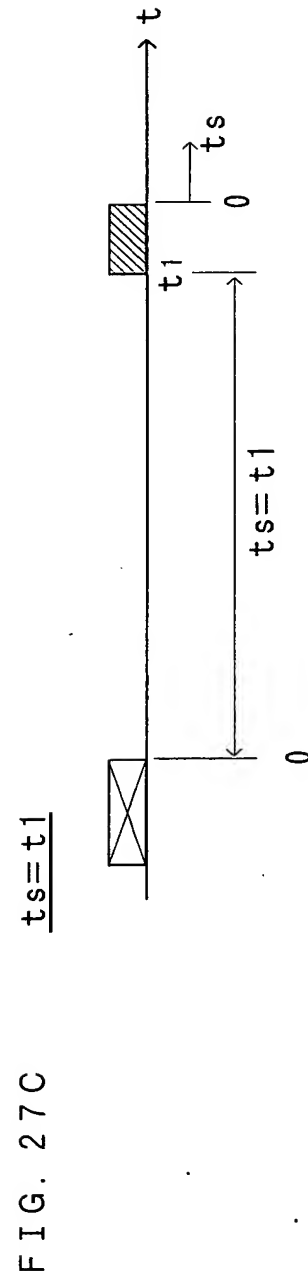
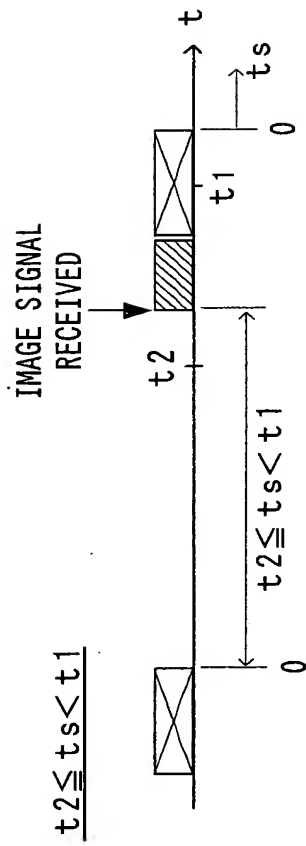
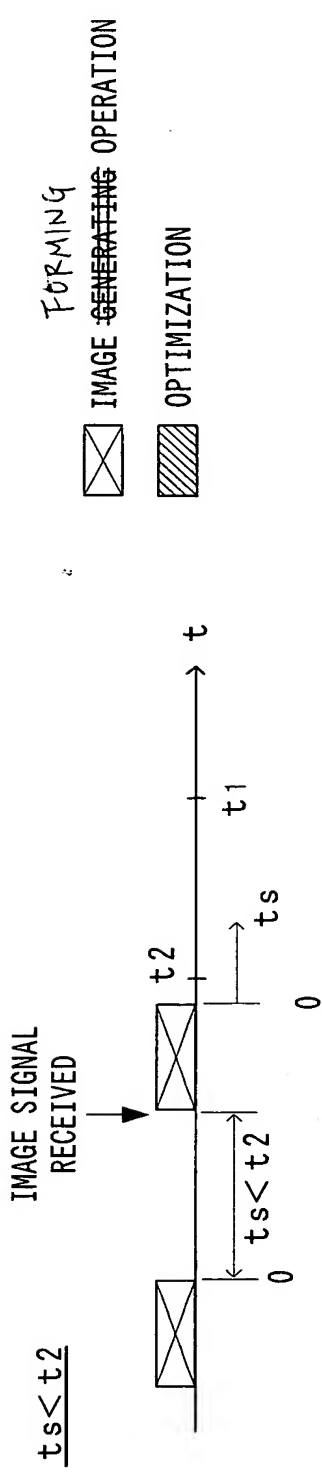
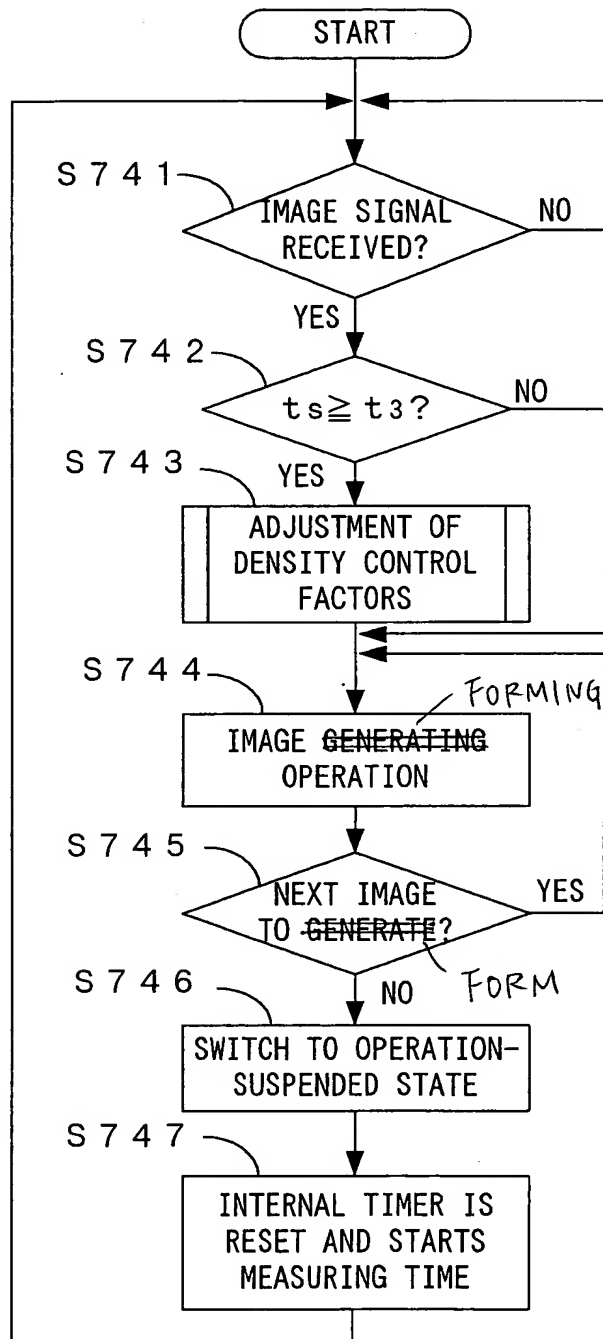


FIG. 28



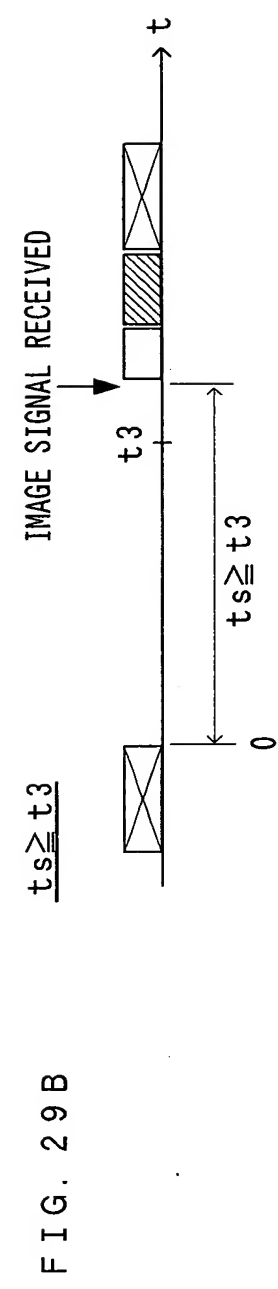
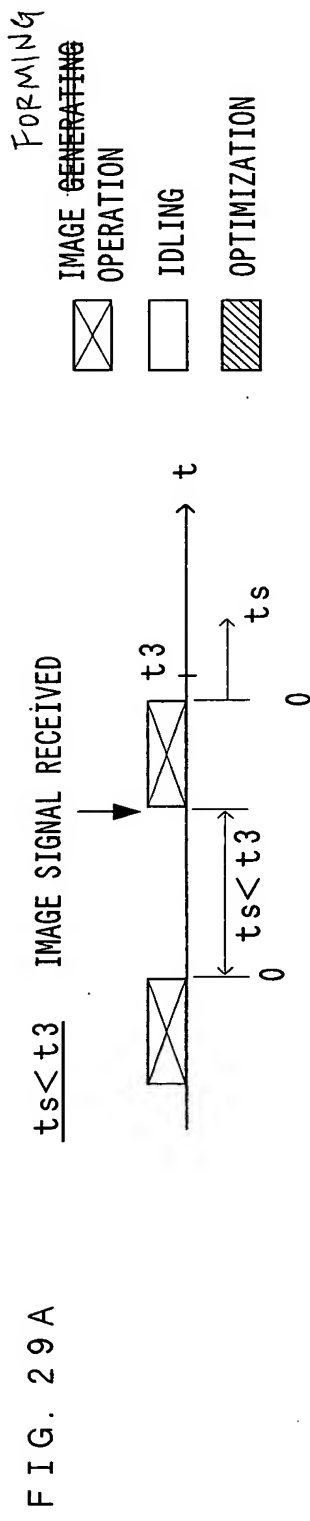


FIG. 30

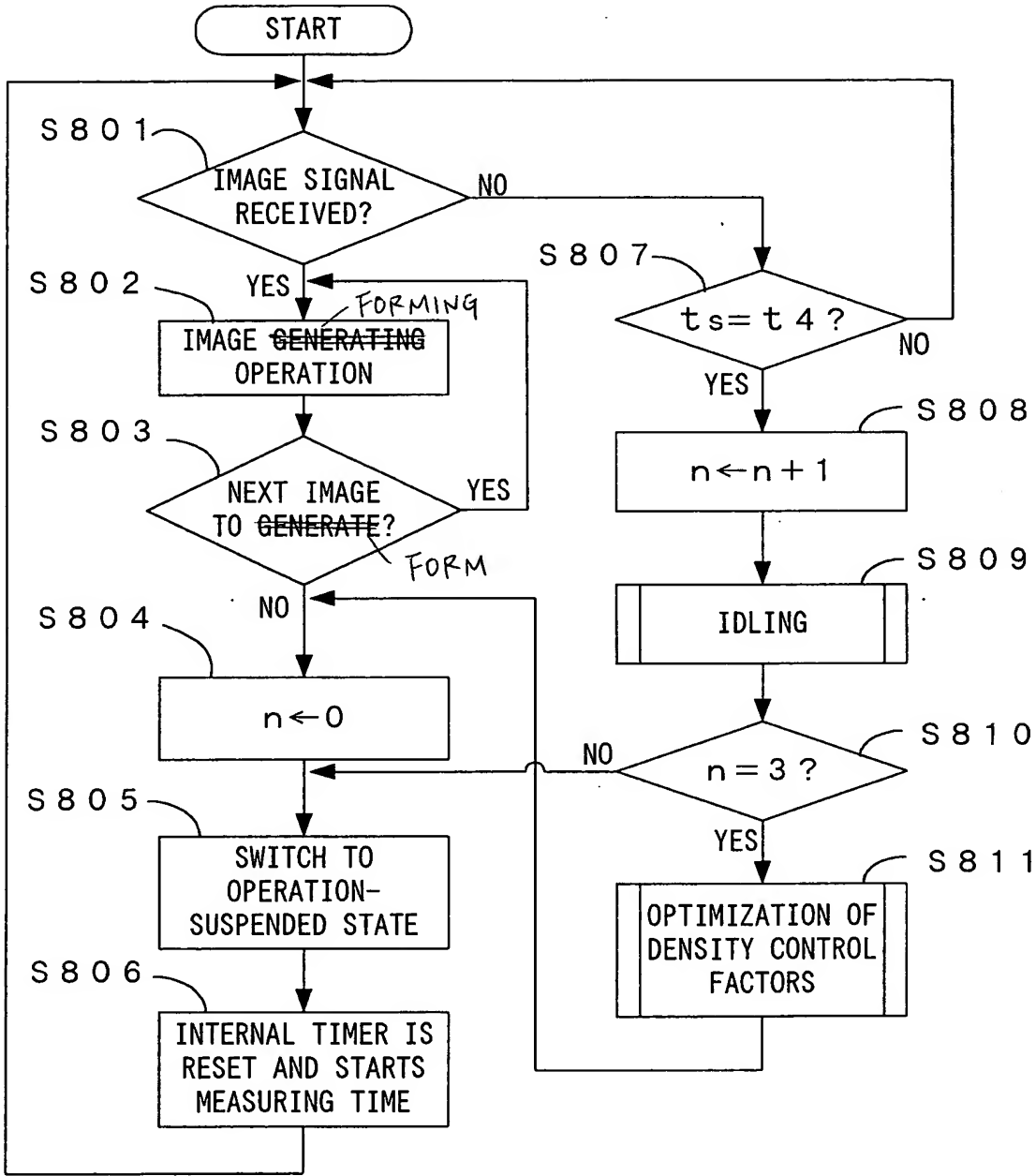


FIG. 32A

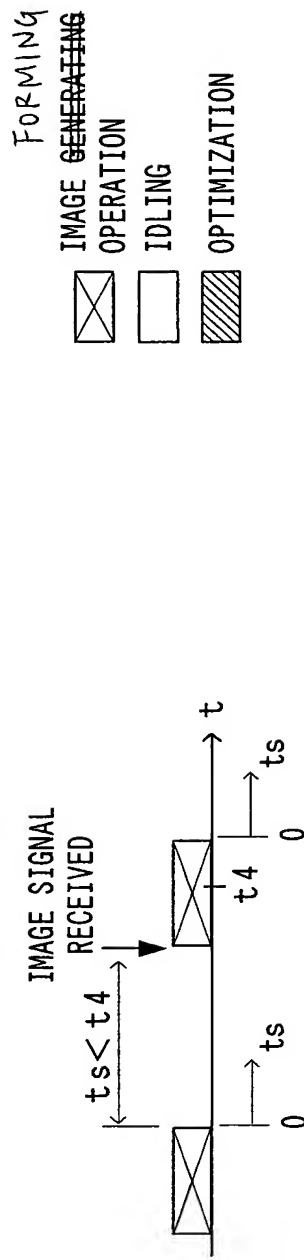


FIG. 32B

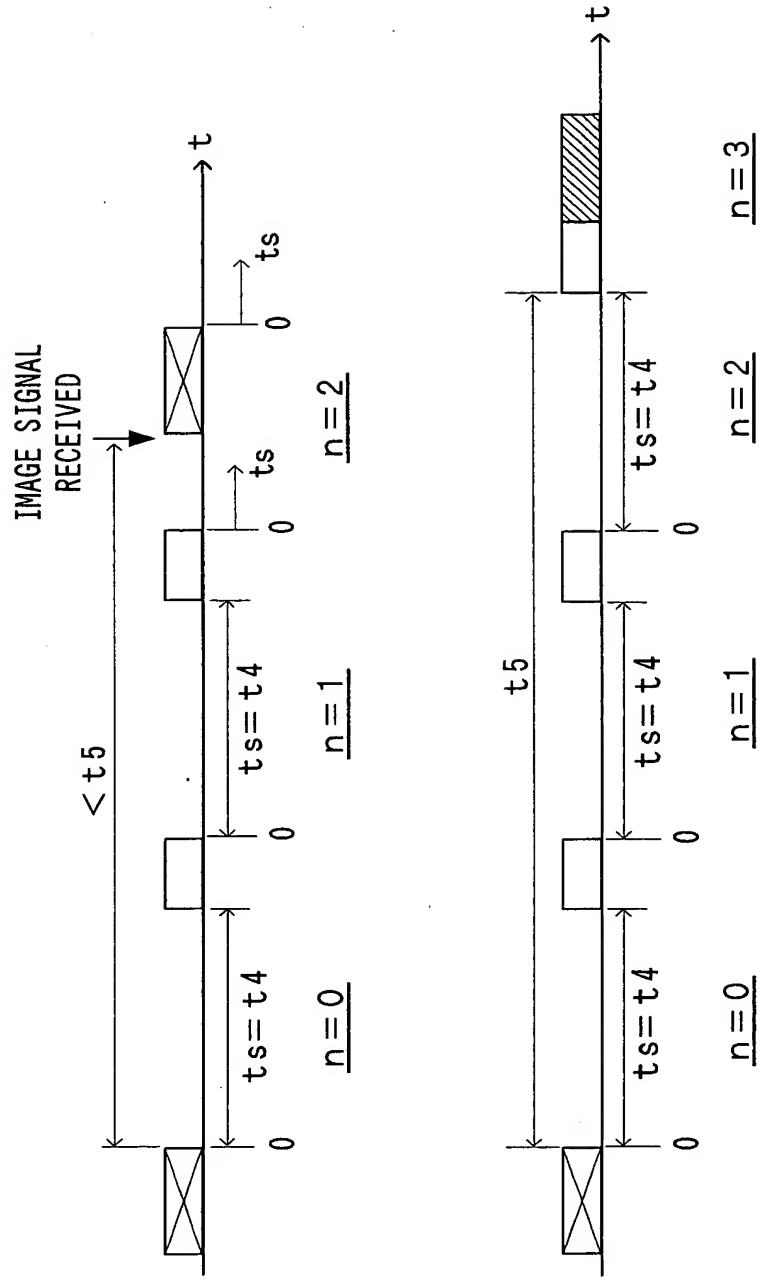


FIG. 32C

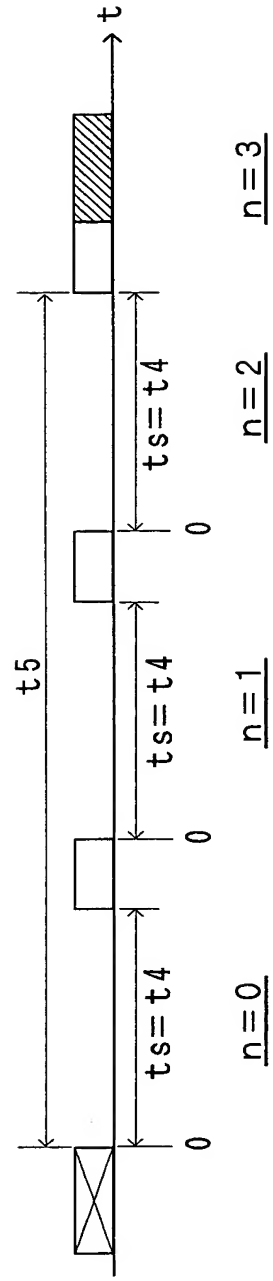
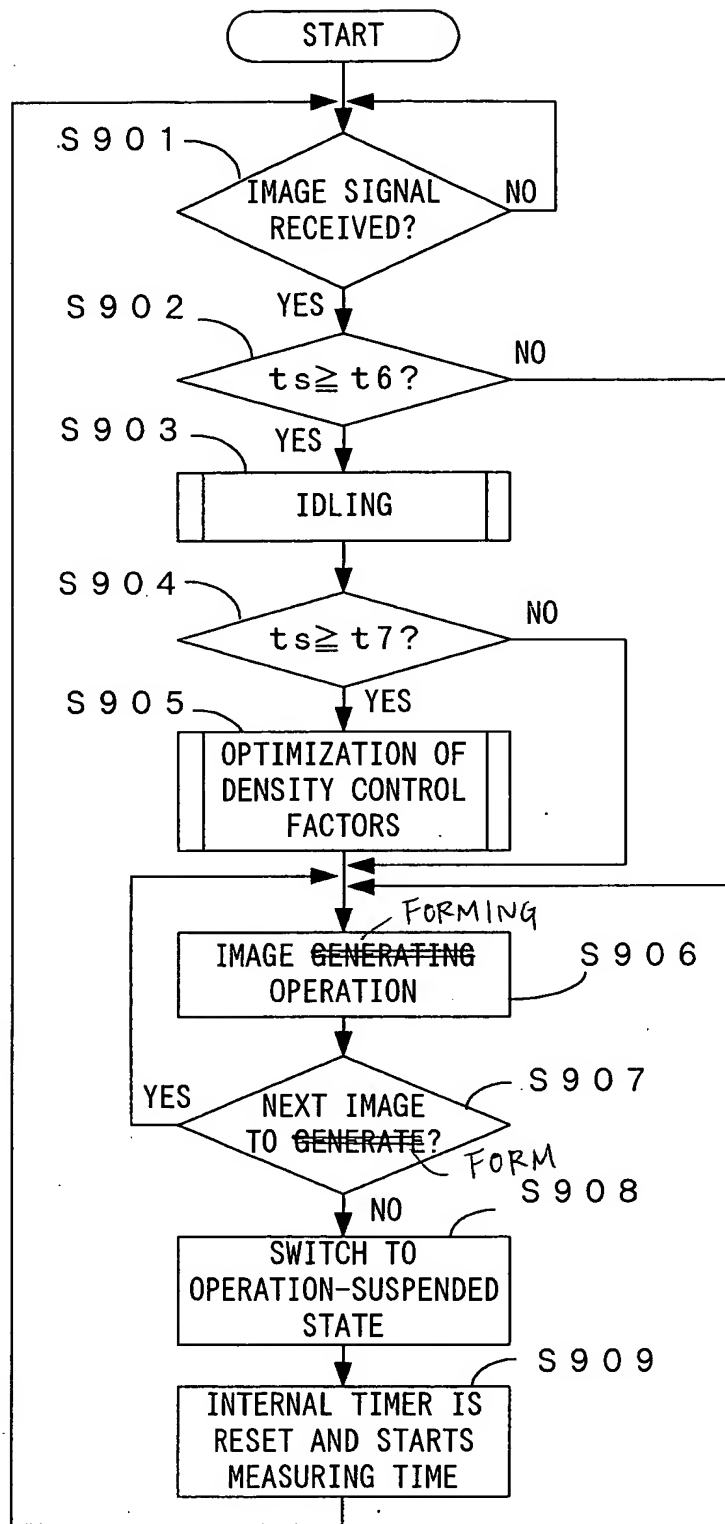


FIG. 33



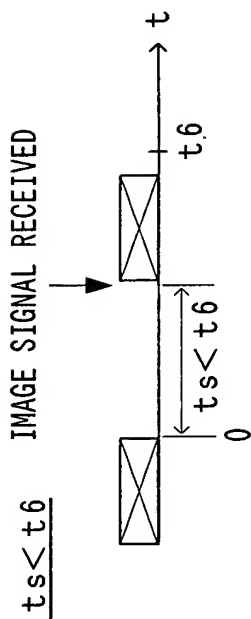
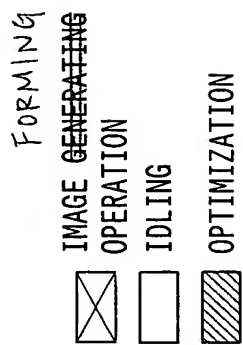


FIG. 34 A

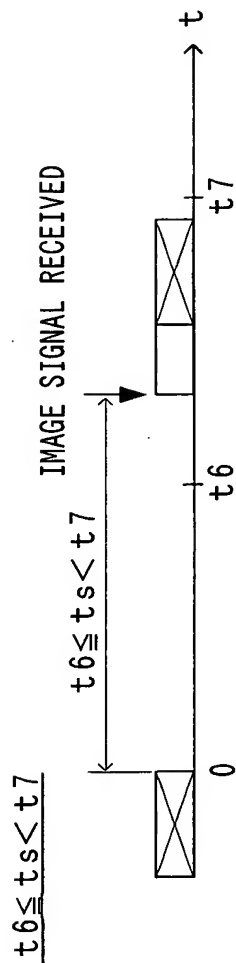


FIG. 34 B

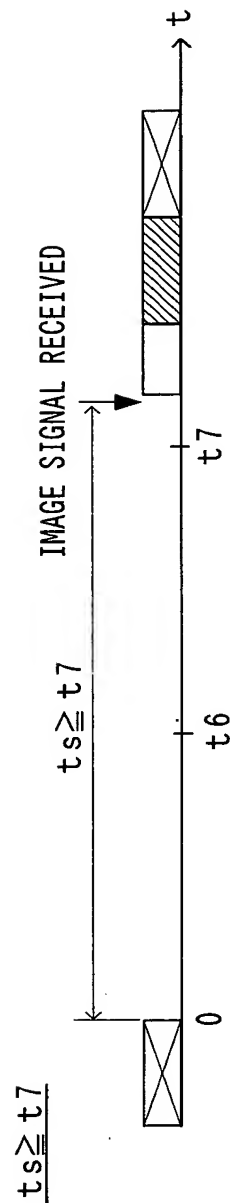


FIG. 34 C

FORMING

IMAGE GENERATING

OPERATION

IDLING

OPTIMIZATION



IMAGE SIGNAL
RECEIVED

$t_w < t_9$

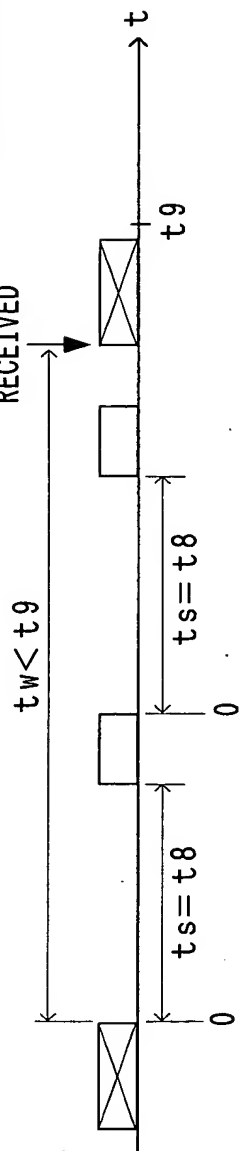


FIG. 35B

$t_w \geq t_9$

